Amendments To The Claims

The listing of claims presented below will replace all prior versions, and listings, of claims in the application.

Listing of claims:

1. **(Currently Amended)** A data write-in method for a flash memory, wherein the flash memory comprises at least two flash chips, and the method ~~includes~~ comprises:

[[a.]] partitioning **[[the]]** physical blocks in the **[[two]]** flash chips to odd logical block addresses and even logical block addresses, respectively;

[[b.]] receiving **[[a]]** data write-in instructions and analyzing **[[the]]** a beginning logical address ~~corresponding to the~~ for writing ~~operation~~ from the received data write-in instructions ~~instruction~~;

[[c.]] obtaining ~~according to the beginning logical address~~ the logical block address needed to be written according to the analyzed beginning logical address;

determining ~~deciding the~~ a parity of the obtained logical block address; ~~needed to be written, and~~

selecting one ~~the corresponding~~ flash chip from ~~between~~ the ~~two~~ flash chips according to the determined parity of the logical block address; ~~needed to be written;~~

~~d. detecting whether the other flash chip needs to be programmed or erased after the~~ directing first programming or erasing instructions ~~erase instruction is directed~~ to the physical blocks ~~block~~ corresponding to the obtained logical block address in the ~~corresponding~~ selected flash chip;

detecting whether the other flash chip needs to be programmed or erased during the first programming or erasing instructions are being processed; if need, the method further comprises:

directing second programming or erasing instructions to the other flash chip of at least two flash chips.

2. **(Cancelled)**

3. **(Currently Amended)** The data write-in method for a flash memory according to claim 1, wherein ~~it further comprises the following step: f.~~ if the other flash chip **does** **[[do]]** not need to be programmed or erased, **the method further comprises:**

~~then judge~~ **judging** whether **the processing of the first programming or erasing instructions** ~~the operation performed to the corresponding physical block in step d~~ is finished**, if yes, returning to the processing of receiving; otherwise, returning to the processing of obtaining**.

4-5. **(Cancelled)**

6. **(Currently Amended)** The data write-in method for a flash memory according to **claim 1** ~~claim 4~~, wherein **the analyzing further comprises:**

~~that: the step b further comprises~~ obtaining the number of sectors needed to be written from the data writing operation instruction.

7. **(Currently Amended)** The data write-in method for a flash memory according to claim 6, ~~wherein that:~~ **the analyzing further comprises** ~~the method further comprises~~

judging whether the data writing operation **instructions** ~~instruction has~~ **have** been finished by subtracting **[[the]]** a number **[[of]]** **for** written sectors from **[[the]]** a number **[[of]]** **for** need-to-be-written-sectors.